

Amendments to the Claims:

Please amend claims 1, 11 and 15 and add the following new claim 22.

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A liquid crystal display device comprising:
- a pair of substrates having a liquid crystal layer disposed therebetween;
 - at least a first conductive layer formed on one of said pair of substrates;
 - at least a first insulating layer formed on the first conductive layer;
 - a plurality of drain signal lines formed on the first insulating layer ~~in~~
~~overlapping relation to the first conductive layer;~~
 - at least a second insulating layer formed on a drain signal line; and
 - at least a second conductive layer formed on the second insulating layer and
elongated substantially along the drain signal line ~~in overlapping relation to the drain~~
~~signal line;~~
- wherein the first conductive layer is elongated substantially along the drain
signal line, wherein the first conductive layer having a plurality of first regions which
overlap with the drain signal line, and a second region which is arranged between
the plurality of first regions and which does not overlap with the drain signal line; and
- wherein the second conductive layer is offset from the overlapping region of
the first conductive layer and the drain signal line proximate to the first regions of the
first conductive layer and overlapped with the drain signal line proximate to the
second region of the first conductive layer.

2. (previously presented) A liquid crystal display device according to claim 1, wherein the second conductive layer maintains an electrical connection around the offset region.

3. (previously presented) A liquid crystal display device according to claim 2, further comprising a plurality of gate signal lines formed on the one of said pair of substrates and crossing the drain signal lines,

wherein the second conductive layer includes a portion having an overlapping relation with the gate signal line.

4. (withdrawn) A liquid crystal display device according to claim 3, wherein the second insulating layer includes a lower insulating layer and an upper insulating layer formed on the lower insulating layer and made of an organic material,

wherein the upper insulating layer is offset from the overlapping region of the first conductive layer and the drain signal line.

5. (withdrawn) A liquid crystal display device according to claim 4, wherein an area of the offset of the second conductive layer is bigger than an area of the offset of the upper insulating layer.

6. (withdrawn) A liquid crystal display device according to claim 3, wherein the second insulating layer includes a lower insulating layer made of an inorganic material and an upper insulating layer formed on the lower insulating layer and made of an organic material,

wherein the upper insulating layer is offset from the overlapping region of the first conductive layer and the drain signal line and the lower insulating layer is not offset from the overlapping region.

7. (withdrawn) A liquid crystal display device according to claim 1, further comprising a plurality of gate signal lines formed on the one of said pair of substrates and crossing the drain signal lines,

wherein the first conductive layer is a gate signal line.

8. (withdrawn) A liquid crystal display device according to claim 7, wherein the gate signal line is separated to plural lines at the region of overlapping of the drain signal line.

9. (previously presented) A liquid crystal display device according to claim 1, further comprising a plurality of counter signal lines formed on the one of said pair of substrates and crossing to the drain signal lines,

wherein the first conductive layer is a counter signal line.

10. (previously presented) A liquid crystal display device according to claim 9, wherein the counter signal line is separated to plural lines at the region of overlapping of the drain signal line.

11. (currently amended) A liquid crystal display device comprising:

a pair of substrates having a liquid crystal layer disposed therebetween;

a plurality of gate signal lines and at least a first conductive layer formed on one of said pair of substrates;

at least a first insulating layer formed on a gate signal line;

a plurality of drain signal lines formed on the first insulating layer and crossing the gate signal lines to form plural pixels ;

at least a second insulating layer formed on a drain signal line;

wherein the first conductive layer is elongated substantially along the drain signal line and ~~having~~ has a plurality of first regions which overlap with the drain signal line, and a second region which is arranged between the plurality of first regions and which does not overlap with the drain signal line a portion overlapping the drain signal line; and

at least a second conductive layer formed on the second insulating layer and elongated substantially along the drain signal line in overlapping relation to the drain signal line ~~and the first conductive layer~~ proximate to the second region of the first conductive layer;

~~the wherein~~ a width of the second conductive layer proximate to the first regions of the first conductive layer at the overlapping region of the drain signal line and the first conductive layer is smaller than a non-overlapping region of the drain signal line and the first conductive layer a width thereof proximate to the second region of the first conductive layer.

12. (previously presented) A liquid crystal display device according to claim 11,

wherein the overlapping region of the drain signal line and the first conductive layer is plural in each of the pixels, and the width of the second conductive layer is smaller in each overlapping region.

13. (previously presented) A liquid crystal display device according to claim 11,

wherein the second conductive layer is offset from the first conductive layer at the overlapping region of the first conductive layer and the drain signal line, and an overlap to the another first conductive layer is arranged on an opposite side of the drain signal line relative to the first conductive layer having an overlapping relation with the drain signal line.

14. (withdrawn) A liquid crystal display device according to claim 11, wherein the second insulating layer includes a lower insulating layer made of an inorganic material and an upper insulating layer formed on the lower insulating layer and made of an organic material,

wherein the upper insulating layer is offset from the overlapping region of the first conductive layer and the drain signal line.

15. (currently amended) A liquid crystal display device comprising:

a pair of substrates having a liquid crystal layer disposed therebetween;

at least a first conductive layer formed on one of said pair of substrates;

at least a first insulating layer formed on the first conductive layer;

a plurality of drain signal lines formed on the first insulating layer in overlapping relation to the first conductive layer, the first conductive layer being elongated substantially along the drain signal line;

at least a second insulating layer formed on a drain signal line; and

at least a second conductive layer formed on the second insulating layer and elongated substantially along the drain signal line in overlapping relation to the drain signal line;

wherein the second conductive layer has a hole at the overlapping region of the first conductive layer and the drain signal line.

16. (previously presented) A liquid crystal display device according to claim 15, further comprising a plurality of gate signal lines formed on the one of said pair of substrates and crossing the drain signal lines,

wherein the second conductive layer includes a portion having an overlapping relation with a gate signal line.

17. (withdrawn) A liquid crystal display device according to claim 15, wherein the second insulating layer includes a lower insulating layer and an upper insulating layer formed on the lower insulating layer and made of an organic material,

wherein the upper insulating layer has a hole as the overlapping region of the first conductive layer and the drain signal line.

18. (withdrawn) A liquid crystal display device according to claim 17, wherein the hole of the second conductive layer is bigger than the hole of the upper insulating layer.

19. (previously presented) A liquid crystal display device according to claim 15, wherein the first conductive layer is separated to plural lines at the region of overlapping of the drain signal line.

20. (withdrawn) A liquid crystal display device according to claim 19, further comprising a plurality of gate signal lines formed on the one of said pair of substrates and crossing the drain signal lines,

wherein the first conductive layer is a gate signal line.

21. (previously presented) A liquid crystal display device according to claim 19, further comprising a plurality of counter signal lines formed on the one of said pair of substrates and crossing the drain signal lines,

wherein the first conductive layer is a counter signal line.

22. (new) A liquid crystal display device according to claim 15, wherein the first conductive layer has a plurality of first regions which overlap with the drain signal line, and a second region which is arranged between the plurality of first regions and which does not overlap with the drain signal line, the second conductive layer having the hole proximate to the first regions of the first conductive layer.